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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/672,010	09/25/2003	Hisac Shibuya	16869S-095400US	7689

20350 7590 03/06/2007
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EXAMINER

LIEW, ALEX KOK SOON

ART UNIT	PAPER NUMBER
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2624

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	03/06/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/672,010

Applicant(s)

SHIBUYA ET AL.

Examiner

Alex Liew

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 10 and 11 is/are allowed.
- 6) ☒ Claim(s) 1-9 and 12-15 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 25 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Allowable Claims

Claims 10 and 11 are allowable.

With regards to claim 10, the examiner cannot find applicable prior art and / or suggestion disclosing weighting a point where a perpendicular of arbitrary two defects from the wafer map passes according to the distance between the two defects and voting the point onto the xy space and detecting (x,y) corresponding to the maximum value on the voted space in combination with the rest of the limitations of claim 10.

With regards to claim 11, see the rationale and rejection for claim 10.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1, 4, 7, 8 and 12 – 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tubin (US pat no 5,982,920) in view of Ikeda (US pat no 7,068,834).

With regards to claim 1, Tubin discloses a defect data analysis method comprising the steps of

obtaining defect positions by inspecting a substrate processed in a process of circuit pattern formation of the substrate (see col. 6 lines 41 – 43 – computing the statistics of the defects requires the position coordinates of the defects),

obtaining distribution of defects on the processed substrate from the defect position information obtained (see fig 2 – 'Distribution statistics' and col. 5 lines 43 – 49) and

classifying the defect distribution characteristics into any one of the following distribution characteristic categories by using the information on the defect position on the processed substrate: repeated defects (see col. 5 lines 61 – 64 – the plot shows the number of times specify defects are repeated), clustered defects (see col. 4 lines 22 – 22), arc-shaped regional defects (see col. 4 lines 4 – 6 – arc shaped defects are radial shapes), radial regional defects (see col. 4 lines 4 – 6), line type regional defects (see col. 4 lines 39 – 50 – elongated defect forms a line type regional defects) and random defect (see col. 3 lines 33 – 34).

But Tubin does not disclose ring / blob type regional defects and displaying classified results by using different colors. Ikeda discloses classifying ring / blob regional defects (see fig 1 – 103) and displaying the classified results by using different colors for respective categories (see fig 11 – under 'Classification category').

It would have been obvious to one having ordinary skill in the art at the time of the invention was made to include displaying classified results by using different colors because to allow the operator to easily differential between each classified defect without looking at the defect type image; a single type defect can occur up to hundreds

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to thousands times, showing the results in color will ease the time needed to correct the defects on the wafer.

With regards to claim 4, Tubin discloses a defect data analysis method as claimed in claim 1, wherein the defects classified into the arc-shaped regional defects are judged to be scratches generated by chemical and mechanical polishing (see col. 4 lines 13 – 16).

With regards to claim 7, see the rationale and rejection for claim 1. In addition, before classifying objects in image to a category, the objects need to be identified first.

With regards to claim 8, Tobin discloses a defect data analysis method as claimed in claim 7, wherein the processing step is realized by displaying the defect information in a wafer map format on the display screen (see fig 1 – the image must be display on the computer monitor in order to be printed out).

With regards to claims 12 – 15, see the rationale and rejection for claim 1. In addition, see figure 2 of Tobin for input means, 'Wafermap data'.

3. Claims 2 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tubin (US pat no 5,982,920) in view of Ikeda (US pat no 7,068,834) as applied to claim 1 further in view of Smilansky (US pat no 7,016,526).

With regards to claim 2, Tobin discloses all of the claim elements / features as discussed above in rejection for claim 1 and incorporated herein by reference and also discloses a defect data analysis method as claimed in claim 1, wherein the arc-shaped regional defects are detected by obtained a center candidate point of the defect distribution from the defect distribution characteristic on the Cartesian coordinates (see col. 4 lines 4 – 6 – arc shaped defects are radial shapes – the position of the defects are also extracted, col. 6 lines 41 – 43 – and the distribution is taken in fig 2 – ‘Distribution statistics’), but fails to disclose extracting the defects from the polar coordinate information. Smilansky discloses extracting the defects from the polar coordinates information on each defect using the center candidate point as an origin (see col. 19 lines 27 – 33 and fig 18). It would have been obvious to one having ordinary skill in the art at the time of the invention was made to include extracting the defects from the polar coordinate information because wafers are known to be circular (see Smilansky fig 18 – radius and angle), converting data from Cartesian to polar coordinates ease data computations, where best Cartesian coordinate system represents rectangular space (see fig 18 – see rectangular grids).

With regards to claim 9, see the rationale and rejection for claim 2.

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4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tubin (US pat no 5,982,920) in view of Ikeda (US pat no 7,068,834) as applied to claim 1 further in view of Li (US pat no 6,130,959).

Tobin discloses all of the claim elements / features as discussed above in rejection for claim 1 and incorporated herein by reference, but fails to disclose creating distribution data on polar coordinate space from information on the Cartesian space and extracting the radial regional defects from polar coordinate space. Lid discloses creating distribution data on the polar coordinate space according to the information on the Cartesian coordinate space of the objects (see fig 8 – shows specimen points in polar coordinate which is converted from the Cartesian coordinate from fig 7) and extracting the radial regional objects from the distribution data on the polar coordinate space (the specimen points are extracted from using reference data points shown in fig 5 and 6). One skill in the art would extract objects or data information using polar coordinate because wafers are known to be circular converting data from Cartesian to polar coordinates ease data computations and does not require coordinate rotation (see Li col. 4 lines 45 – 52).

5. Claims 3 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tubin (US pat no 5,982,920) in view of Ikeda (US pat no 7,068,834) as applied to claim 1 further in view of Maruo (US pat no 6,408,105).

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With regards to claim 3, Tobin discloses all of the claim elements / features as discussed above in rejection for claim 1 and incorporated herein by reference, but fails to disclose perpendicular bisectors of straight lines connecting arbitrary two defects among the defects distributed on the processed substrate. Maruo discloses center point is extracted as a point having more intersections of perpendicular bisectors of straight lines connecting arbitrary two points (see fig 5 – points are P0 and P2). One skill in the art would connect two arbitrary points because to find the distance between individual defects so information is send to the system or operator and have the system or operator perform corrections on the defect to improve quality inspection.

With regards to claim 6, see the rationale and rejection for claim 3. In addition, figure 6 and 7 of Maruo are the polar coordinate transformed space of figure 5.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alex Liew whose telephone number is (571)272-8623.

The examiner can normally be reached on 9:30AM - 7:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph Mancuso can be reached on (571)272-7695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Alex Liew
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2/22/07



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